



FURTHER PARTICULARS

- Job Title:** Research Associate x 2
- Department:** Electronic and Electrical Engineering
- Reports to:** Prof. George Pavlou
- Grade:** The posts are Grade 7, starting salary £33,353 per annum and maximum salary £40,313 per annum, both including London Allowance which is currently £2,919 per annum.
- Period of Appointment:** 12 months from May / June 2015 initially

Main Purpose

Undertaking research, design and development in the context of one of our EU H2020 or EPSRC-funded projects, which focus on content-oriented Internet architectures. Our projects aim to investigate and develop a communication and information infrastructure based on the principles of Information-Centric Networks (ICNs) with a possible extension to opportunistic or Delay-Tolerant Networks (DTN) environments in one of the projects.

Duties and Responsibilities

- To participate in the design, validation, evaluation, but mainly implementation of the proposed architecture and related algorithms and protocols. The following areas are of special importance in this project:
 - Content naming and information exposure through content names.
 - Content resolution procedures and routing algorithms in ICN and DTN architectures.
 - Information-Centric Networking and Opportunistic and Delay-Tolerant Networking architectures.
 - Traffic engineering, resource management and congestion control in ICN environments.

- Implementation, validation and evaluation of the above concepts in either simulation or testbed environments.
- To contribute to the drafting and submitting of papers to high quality peer reviewed conferences and journals.
- To contribute to the preparation and drafting of research bids and proposals.
- To contribute towards and produce reports and deliverables as required by the European Commission.
- To contribute to the overall activities of the research team and department as required. To contribute to the induction and direction of other research staff and students as requested.
- The postholders should be prepared to travel for short periods (usually for 1-3 days but, occasionally, this could be for up to two weeks) around Europe and possibly further abroad. Occasional periods of late or weekend working may be required.
- The postholders will carry out any other duties as are within the scope, spirit and purpose of the job as requested by the line manager or Head of Department. As duties and responsibilities change, the job description will be reviewed and amended in consultation with the postholders.
- The postholders will actively follow UCL policies including Equal Opportunities and Race Equality policies.
- The postholders will maintain an awareness and observation of Fire and Health & Safety Regulations.
- The postholders will be required to relocate to London.

Information about the Communications and Information Systems Group may be found at: <http://www.ee.ucl.ac.uk/research/cominfosys/>

**Information about the department may be found at:
<http://www.ee.ucl.ac.uk/>**

**Further information regarding UCL may be found at:
<http://www.ucl.ac.uk/>**

Person Specification for the Post of Research Associate

Knowledge – including Qualifications

- Strong (first or upper second class) undergraduate degree in Electronic Engineering or Computer Science (essential).
- PhD in Computer Science, Electronic Engineering or a related field. (essential)
- Knowledge of Information-Centric Networking (ICN) (essential).
- Knowledge of Opportunistic and Delay-Tolerant Networking (DTN) (essential).
- Knowledge of resource management techniques and congestion control protocols (essential).
- Hands on and implementation experience in one or more of the following areas: content-oriented architectures; information-centric networking; opportunistic and delay-tolerant networks; resource management and congestion control (essential).

Skills

- Strong programming skills in C, C++ and Java (essential).
- Effective written and verbal communication skills (essential).
- A good publication record in the field (essential).
- Experience in managing and participating in EU-funded projects.(desirable)
- Algorithm design skills (desirable).

Experience

- Substantial experience of working with TCP/IP networks (essential).
- Experience of developing and conducting network experiments in simulation environments and/or laboratory testbeds or real-world networks (essential).

Personal Qualities

- Commitment to high quality research (essential).
- The candidates should be able to manage their own time efficiently and take responsibility for working to deadlines (essential).

HOW TO APPLY

Interested applicants are encouraged to make Informal enquiries about the post to Dr Ioannis Psaras (i.psaras@ucl.ac.uk) or Prof. George Pavlou (g.pavlou@ucl.ac.uk, tel. 020 7679 3985).

Further particulars and details of how to apply are available at www.ee.ucl.ac.uk/vacancies

All applications should be submitted via UCL online recruitment system at the following link:

<http://www.ucl.ac.uk/hr/jobs/>

Job Reference: 1454405

If you experience any problems please contact Paul McKenna at p.mckenna@ucl.ac.uk quoting reference 1454405

The closing date for applications is 29th March 2015

UCL Taking Action for Equality